

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	342	712/200.ccls.	USPAT	OR	ON	2004/11/23 13:35
L2	236	712/220.ccls.	USPAT	OR	ON	2004/11/23 13:35
L3	69	(circular with buffer) same allocat\$5 same available	USPAT; EPO; JPO; DERWENT	OR	ON	2004/11/23 13:35
L4	25	previous same (allocation with buffer) same input	USPAT	OR	ON	2004/11/23 13:35
L5	59	(shift near3 register) with enable with (independent sepeate individual)	USPAT; EPO; JPO	OR	ON	2004/11/23 13:35
L6	2	(round-robin) same allocation same availabl\$7 same buffer	USPAT; EPO; JPO; DERWENT	OR	ON	2004/11/23 13:35
L7	56	(round with robin) same allocation same buffer	USPAT; EPO; JPO	OR	ON	2004/11/23 13:35
L8	20	"buffer allocation" same current\$2 same input	USPAT; EPO; JPO	OR	ON	2004/11/23 13:35
L9	9	(round with robin) with allocation with buffer	USPAT; EPO; JPO	OR	ON	2004/11/23 13:35
L10	122	"buffer allocation" same current\$2	USPAT; EPO; JPO	OR	ON	2004/11/23 13:35
L11	122	"buffer allocation" same current\$2	USPAT; EPO; JPO	OR	ON	2004/11/23 13:36
L12	21	"buffer allocation" same previous	USPAT; EPO; JPO	OR	ON	2004/11/23 13:36
L13	21	circular with buffer with pointer with previous	USPAT; EPO; JPO	OR	ON	2004/11/23 13:36
L14	713	circular with buffer with pointer	USPAT	OR	ON	2004/11/23 13:36
L15	19	circular with buffer with pointer with previous	USPAT	OR	ON	2004/11/23 13:36
L16	16	circular with buffer with pointer with allocat\$6	USPAT	OR	ON	2004/11/23 13:36
L17	5	cascad\$4 same (allocation with buffer)	USPAT	OR	ON	2004/11/23 13:36
L18	10	(previous with input) same (allocation with buffer)	USPAT	OR	ON	2004/11/23 13:36
L19	116	previous same (allocation with buffer)	USPAT	OR	ON	2004/11/23 13:36
S1	2199	buffer with allocation	USPAT	OR	ON	2003/09/21 20:29
S2	1	buffer with allocation with cascade	USPAT	OR	ON	2003/09/08 00:17
S3	2	(buffer with allocation) same cascade	USPAT	OR	ON	2003/09/08 00:17

S4	5	(buffer with allocation) same cascad\$4	USPAT	OR	ON	2003/09/08 00:17
S5	629	"buffer allocation"	USPAT	OR	ON	2003/09/08 12:23
S6	67	"buffer allocation" same input same output	USPAT	OR	ON	2003/09/11 13:20
S7	0	"buffer allocation" same "AND" same "or"	USPAT	OR	ON	2003/09/08 12:29
S8	7	"buffer allocation" same input same output same available	USPAT	OR	ON	2003/09/08 12:29
S9	2	"4510581".URPN	USPAT	OR	OFF	2003/09/09 17:11
S10	2303	cascad\$4 same (available enable) same output	USPAT	OR	ON	2003/09/10 10:45
S11	1125	cascad\$4 same (available enable) same output same circuit	USPAT	OR	ON	2003/09/10 10:46
S12	850	cascad\$4 same (available enable) same output same circuit same input	USPAT	OR	ON	2003/09/10 10:50
S13	229	cascad\$4 same (available enable) same output same circuit same input same count\$4	USPAT	OR	ON	2003/09/10 12:17
S14	197	cascad\$4 same (available enable) same output same circuit same input same select\$3	USPAT	OR	ON	2003/09/10 12:17
S15	26	cascad\$4 same (available enable) same output same circuit same input same increment\$2	USPAT	OR	ON	2003/09/10 12:20
S16	2	cascad\$4 same iterative same increment\$2	USPAT	OR	ON	2003/09/10 14:00
S17	4867	(shift near3 register) with enable	USPAT	OR	ON	2003/09/10 14:01
S18	54	(shift near3 register) with enable with (independent seperate individual)	USPAT	OR	ON	2004/05/30 19:50
S19	250	(circular with buffer) same allocat\$5	USPAT	OR	OFF	2003/09/11 11:51
S20	59	(circular with buffer) same allocat\$5 same available	USPAT	OR	ON	2004/05/30 19:50
S21	2741	interleaved with memory	USPAT	OR	ON	2003/09/11 11:59
S22	6	(interleaved with memory) same allocat\$5 same available	USPAT	OR	ON	2003/09/11 12:00
S23	18	"buffer allocation" same previous	USPAT	OR	ON	2004/05/30 19:49
S24	111	"buffer allocation" same current\$2	USPAT	OR	ON	2004/05/30 19:49
S25	29	"buffer allocation" same current\$2	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/11 13:22

S26	6	"buffer allocation" same previous	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/11 13:23
S27	29	"buffer allocation" same current\$2	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/11 13:23
S28	19	"buffer allocation" same current\$2 same input	USPAT	OR	ON	2004/05/30 19:50
S29	5	"buffer allocation" same current\$2 same input	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/11 15:05
S30	0	"buffer allocation" same (current\$2 with select\$4)	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/11 15:06
S31	3	"buffer allocation" same (current\$2 with select\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/11 15:06
S32	2	"buffer allocation" same (current\$2 with input)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/09/11 15:06
S33	454	(available with vector) same buffer	US-PGPUB; USPAT	OR	ON	2003/09/15 12:40
S34	23	(available with vector) same buffer same allocat\$4	US-PGPUB; USPAT	OR	ON	2003/09/15 15:14
S35	5	(pickup start begin) with (left-off leftoff)	US-PGPUB; USPAT	OR	ON	2003/09/15 15:16
S36	4	(pickup start begin) same startover	US-PGPUB; USPAT	OR	ON	2003/09/15 15:21
S37	21055	(pickup start begin) with last	US-PGPUB; USPAT	OR	ON	2003/09/15 15:21
S38	1803	(pickup start begin) with last with (memory buffer)	US-PGPUB; USPAT	OR	ON	2003/09/15 15:24
S39	77	(pickup start begin) with last with (memory buffer) with availabl\$4	US-PGPUB; USPAT	OR	ON	2003/09/15 15:24
S40	956	((begin start) with last) same delay	USPAT	OR	ON	2003/09/16 10:18
S41	46	((begin start) with last with device) same delay	USPAT	OR	ON	2003/09/16 10:18
S42	28	(inverter with "and gate") same allocation	USPAT	OR	ON	2003/09/21 20:29

S43	2	(inverter with "and-gate" with "or-gate") same allocation	USPAT	OR	ON	2003/09/21 20:30
S44	12	(inverter with "and gate" with "or gate") same allocation	USPAT	OR	ON	2003/09/21 20:32
S45	1	(inverter with "and gate" with "or gate") same allocation same availabl\$7	USPAT	OR	ON	2003/09/21 20:32
S46	1	(inverter with "and gate" with "or gate") same allocation same availabl\$7	USPAT	OR	ON	2003/09/21 20:33
S47	21	(round-robin) same allocation same availabl\$7	USPAT	OR	ON	2003/09/21 20:34
S48	41	(round with robin) same allocation same availabl\$7	USPAT	OR	ON	2003/09/21 22:22
S49	2	(round with robin) same allocation same buffer same availabl\$7	USPAT	OR	ON	2003/09/21 20:34
S50	2	(round-robin) same allocation same availabl\$7 same buffer	USPAT	OR	ON	2004/05/30 19:50
S51	44	(round with robin) same allocation same buffer	USPAT	OR	ON	2004/05/30 19:50
S52	8	(round with robin) with allocation with buffer	USPAT	OR	ON	2004/05/30 19:50
S53	38	circular with allocation with buffer	USPAT	OR	ON	2003/09/21 22:50
S54	339	712/200.ccls.	USPAT	OR	ON	2004/08/23 03:19
S55	229	712/220.ccls.	USPAT	OR	ON	2004/08/23 03:19
S56	65	(circular with buffer) same allocat\$5 same available	USPAT; EPO; JPO; DERWENT	OR	ON	2004/08/23 03:20
S57	25	previous same (allocation with buffer) same input	USPAT	OR	ON	2004/08/23 03:20
S58	59	(shift near3 register) with enable with (independent sepearte individual)	USPAT; EPO; JPO	OR	ON	2004/08/23 03:20
S59	2	(round-robin) same allocation same availabl\$7 same buffer	USPAT; EPO; JPO; DERWENT	OR	ON	2004/08/23 03:20
S60	55	(round with robin) same allocation same buffer	USPAT; EPO; JPO	OR	ON	2004/08/23 03:20
S61	19	"buffer allocation" same current\$2 same input	USPAT; EPO; JPO	OR	ON	2004/08/23 03:20
S62	9	(round with robin) with allocation with buffer	USPAT; EPO; JPO	OR	ON	2004/08/23 03:20
S63	121	"buffer allocation" same current\$2	USPAT; EPO; JPO	OR	ON	2004/08/23 03:20
S64	20	"buffer allocation" same previous	USPAT; EPO; JPO	OR	ON	2004/08/23 03:20

S65	21	circular with buffer with pointer with previous	USPAT; EPO; JPO	OR	ON	2004/08/23 03:20
S66	694	circular with buffer with pointer	USPAT	OR	ON	2004/08/23 03:20
S67	15	circular with buffer with pointer with allocat\$6	USPAT	OR	ON	2004/08/23 03:20
S68	19	circular with buffer with pointer with previous	USPAT	OR	ON	2004/08/23 03:20
S69	5	cascad\$4 same (allocation with buffer)	USPAT	OR	ON	2004/08/23 03:20
S70	115	previous same (allocation with buffer)	USPAT	OR	ON	2004/08/23 03:20
S71	10	(previous with input) same (allocation with buffer)	USPAT	OR	ON	2004/08/23 03:19